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INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				Applicant: Jan Louis Josephina SERVAES, et al.			
				Filing Date: July 23, 2001	Group: Not Assigned		
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document	Date	Country	Class	Sub-class	Translation Yes/No
WF3		EP 901 221 A1	03/10/99	European			Yes
WF3		WO 99/39433	08/05/99	World Office PCT			Yes
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
WF3		B. Schweber, "Analog Front Ends Bridge the XDSL-to-Real-World Chasm", EDN ELECTRICAL DESIGN NEWS, Massachusetts, US, Vol. 44, No. 7, 1 April 1999, pp. 48, 50, 52, 54, 56, 58, 60, 62, 64, XP000899059					
		E. Nash, "Line-Driver Design for Broadband Communications Applications", ELECTRONIC DESIGN, US, Cleveland, Ohio, Vol. 45, No. 27, 1 December 1997, pp. 81-82, 84, 86, 88, XP000755760					
		M. Steffes et al, "Optimizing Performance in an XDSL Line Driver", ELECTRONIC DESIGN, Cleveland, Ohio, US, Vol. 47, No. 8, 19 April 1999, pp. 44, 46, 48, 52, 54, 56, 58 XP000893613					
✓		E. Nash, "Saving Dynamic Power in HDSL Line Driving Applications", ELECTRONIC DESIGN, US Cleveland Ohio, Vol. 45, No. 12, June 9, 1997, page 88, 90, 92, XP000731500					
EXAMINER: <i>W. F. B. Garguilo</i>		DATE CONSIDERED: 8/16/04					
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